

Walchand College of Engineering

(Government Aided Autonomous Institute)

Vishrambag, Sangli. 416415



**Credit System for
S.Y. B.Tech. (Electronics Engineering)
Sem-III and IV**

2022-23



Walchand College of Engineering

(Government Aided Autonomous Institute)

Credit System for S.Y. B.Tech. (Electronics Engineering) Sem-III AY 2022-23

Sr.No.	Category	Course Code	Course Name	L	T	P	I	Hrs	Cr	MSE/LA1	ISE/LA2	ESE	Ext	
Professional Core (Theory)														
1	ES	6MA202	Probability and Statistics	2	0	0	0	2	2	30	20	50		
2	PC	6EN201	Electronic Circuit Analysis and Design-I	3	0	0	0	3	3	30	20	50		
3	PC	6EN202	Circuit Theory	3	1	0	0	4	4	30	20	50		
4	PC	6EN203	Digital Electronics	3	0	0	0	3	3	30	20	50		
5	PC	6EN204	Electronic Instrumentation	2	0	0	0	2	2	30	20	50		
Professional Core (Lab)														
6	PC	6EN255	Data Structures and Algorithms Lab	0	0	2	2	4	3	30	30	40	POE	
7	PC	6EN251	Electronic Circuit Analysis and Design-I Lab	0	0	2	0	2	1	30	30	40	POE	
8	PC	6EN252	Digital Electronics Lab	0	0	2	0	2	1	30	30	40	POE	
9	PC	6EN253	Python Programming	0	0	2	1	3	2	30	30	40		
AICTE Mandatory Courses														
10	MC	5IC201	Environmental Science	2	0	0	0	2	0	30	20	50		
Total				15	1	8	3	27	21					

Notes:

For Theory courses: There shall be MSE, ISE and ESE. The ESE is a separate head of passing.

For Lab courses: There shall be continuous assessment (LA1, LA2, ESE). The ESE is a separate head of passing. The Y in the PoE indicates external component for ESE.

Minimum two AICTE mandatory courses need to be completed for award of degree.

For further details, refer to Academic and Examination rules and regulations.



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Credit System for S.Y. B.Tech. (Electronics Engineering) Sem-IV AY 2022-23

Sr.No.	Category	Course Code	Course Name	L	T	P	I	Hrs	Cr	MSE/LA1	ISE/LA2	ESE	Ext	
Professional Core (Theory)														
1	PC	6EN221	Electronic Circuit Analysis and Design-II	3	0	0	0	3	3	30	20	50		
2	PC	6EN222	Signals and Systems	3	1	0	0	4	4	30	20	50		
3	PC	6EN223	Communication Engineering I	3	0	0	0	3	3	30	20	50		
4	PC	6EN224	Microcontroller and Peripheral Interfacing	3	0	0	0	3	3	30	20	50		
5	PC	6EN225	Control Systems	3	0	0	0	3	3	30	20	50		
Professional Core (Lab)														
6	PC	6EN271	Electronic Circuit Analysis and Design-II Lab	0	0	2	0	2	1	30	30	40	POE	
7	PC	6EN273	Communication Engineering I Lab	0	0	2	0	2	1	30	30	40	OE	
8	PC	6EN274	Microcontroller and Peripheral Interfacing Lab	0	0	2	0	2	1	30	30	40	POE	
9	PC	6EN275	Control Systems Lab	0	0	2	0	2	1	30	30	40		
Total				15	1	8	0	24	20					

Notes:

For Theory courses: There shall be MSE, ISE and ESE. The ESE is a separate head of passing.

For Lab courses: There shall be continuous assessment (LA1, LA2, ESE). The ESE is a separate head of passing. The Y in the PoE indicates external component for ESE.

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