

# **Walchand College of Engineering**

*(Government Aided Autonomous Institute)*

Vishrambag, Sangli. 416415



## **Course Content for M. Tech. (Power System Engineering) 2022-24**

PG Coordinator

HOD (Electrical Engg.)

# **Semester- I**

# **Professional Core (Theory)**

# **Courses**

## Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

**AY 2022-23**

### Course Information

<b>Programme</b>	M.Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem I
<b>Course Code</b>	6PS501
<b>Course Name</b>	Digital Protection of Power System
<b>Desired Requisites:</b>	Power System Protection

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	-	30	20	50	100
<b>Credits: 3</b>					

### Course Objectives

<b>1</b>	To make students understand digital techniques for realizing various needs of protection.
<b>2</b>	To strengthen the concepts in power system protection.
<b>3</b>	To develop the skills necessary to analyze, design and implement digital protective relays.

### Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Interpret the performance of devices like CT, PT and relays used in digital protection of Power Systems.	III	Applying
CO2	Analyse the use of digital systems for protection of different parts of power system.	IV	Analyzing
CO3	Estimate and Justify settings of relays for protection of different parts of power system.	V	Evaluating
CO4	Design analog/digital protection scheme for simple electrical systems.	VI	Creating

Module	Module Contents	Hours
I	<b>Review of Relaying Schemes</b> Protection schemes for alternator, transformer, bus bar and induction motors. Transmission line protection using over current- time graded and current graded schemes, drawbacks of these schemes, differential & distance schemes, Electromagnetic CT and PT.	6
II	<b>Comparators</b> <b>a. Dual Input Comparator:</b> Amplitude comparator, phase comparator, duality between amplitude and phase comparators, cosine-type and sine type phase comparators, coincidence type phase comparator. <b>b. Multi Input Comparator:</b> Amplitude comparator, phase comparator.	4
III	<b>Over Current Relays</b> Different time-current characteristics of over current relay, Microprocessor/microcontroller based over current relay, Directional over current relay and its implementation using microprocessor/microcontroller-based scheme.	8

IV	<b>Differential Relays</b> Circulating current differential protection, percentage differential protection of power transformers, effect of magnetizing inrush, effect of over voltage inrush, hardware and software used for digital protection of transformer.	8
V	<b>Distance Protection Relays</b> Microprocessor/microcontroller-based impedance, reactance and admittance relays, and measurement of R and X. Quadrilateral characteristics. Digital protection scheme based upon fundamental frequency signals, hardware and software design.	8
VI	<b>Recent Developments in Digital Protection</b> Digital Relaying techniques based on modern tools of digital signal processing like DFT, Haar Transform, WT etc.	4
<b>Textbooks</b>		
1	Badri Ram, D.N. Vishwakarma, "Power System Protection and Switchgear", TMH, 2004.	
2	Y.G. Paithankar, S.R. Bhide, "Fundamentals of Power System Protection", PHI, 2003.	
<b>References</b>		
1	L.P. Singh, "Digital Protection", New Age, Second Edition, 2004.	
2	A.G. Phadke, J.S. Thorp, "Computer Relaying for Power Systems", Wiley India, II Edi., 2012.	
<b>Useful Links</b>		
1	<a href="https://nptel.ac.in/courses/108/107/108107167/">https://nptel.ac.in/courses/108/107/108107167/</a>	
2	<a href="https://nptel.ac.in/courses/108/105/108105167/">https://nptel.ac.in/courses/108/105/108105167/</a>	

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1	2					
CO2				3		
CO3			2			
CO4		2				1

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

<b>Assessment</b>
<p>The assessment is based on MSE, ISE and ESE. MSE shall be typically on modules 1 to 3. ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO. ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6. For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)</p>

## Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

**AY 2022-23**

### Course Information

<b>Programme</b>	M.Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem I
<b>Course Code</b>	6PS502
<b>Course Name</b>	Application of Power Electronics to Power system
<b>Desired Requisites:</b>	Power System Engineering, Power Electronics

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	-	30	20	50	100
<b>Credits: 3</b>					

### Course Objectives

<b>1</b>	To make students understand concept of FACTs envisages the use of power electronics to improve system operation by fast & reliable control.
<b>2</b>	To cover concepts of FACTs including the description, principle of working and analysis of various FACTs controllers.
<b>3</b>	To strengthen the control of FACTs and system interactions.

### Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Explain necessity, operating principals and benefits of FACTs devices.	II	Understanding
CO2	Choose the suitable FACTs device/controller for particular application.	III	Applying
CO3	Analyze the characteristics of FACTs Controllers and effect of location of the controller on Power System.	IV	Analyzing

Module	Module Contents	Hours
I	<b>Introduction</b> The concept of flexible AC transmission - reactive power control in electrical power transmission lines -uncompensated transmission line - series and shunt compensation. Overview of FACTS devices - Static Var Compensator (SVC) - Thyristor Switched Series capacitor (TCSC) - Unified Power Flow controller (UPFC) - Integrated Power Flow Controller (IPFC).	6
II	<b>Static VAR Compensator (SVC) and Applications</b> Voltage control by SVC - advantages of slope in dynamic characteristics - influence of SVC on system voltage. Applications - enhancement of transient stability - steady state power transfer - enhancement of power system damping - prevention of voltage instability.	6
III	<b>Thyristor Controlled Series Capacitor (TCSC) and Applications</b> Operation of the TCSC - different modes of operation - modelling of TCSC - variable reactance model - modelling for stability studies. Applications - improvement of the system stability limit - enhancement of system damping - voltage collapse prevention.	6
IV	<b>Emerging FACTS Controllers I</b> Static Synchronous Compensator (STATCOM) - operating principle, V-I characteristics	6

V	<b>Emerging FACTS Controllers II</b> Unified Power Flow Controller (UPFC) - Principle of operation - modes of operation -applications - modeling of UPFC for power flow studies	6
VI	<b>Co-Ordination of FACTS Controllers</b> FACTS Controller interactions - SVC-SVC interaction - co-ordination of multiple controllers using linear control techniques - Quantitative treatment of control.	6
<b>Textbooks</b>		
1	R. Mohan Mathur, Rajiv. K. Varma, "Thyristor - Based Facts Controllers for Electrical Transmission Systems", IEEE press and John Wiley & Sons Inc., 2002.	
<b>References</b>		
1	Purkait and Bandyopadhyay " <i>Electrical Machines</i> ", Oxford University Press, 1st Edition, 2017 A.T. John, "Flexible AC Transmission System", Institution of Electrical and Electronic Engineers (IEEE), 1999.	
2	Narain G.Hingorani, Laszlo. Gyugyl, "Understanding FACTS Concepts and Technology of Flexible AC Transmission System", Standard Publishers, Delhi, 2001.	
<b>Useful Links</b>		
1	<a href="https://nptel.ac.in/courses/108/107/108107114/">https://nptel.ac.in/courses/108/107/108107114/</a>	

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1			1			
CO2				2		
CO3						2

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

<b>Assessment</b>
<p>The assessment is based on MSE, ISE and ESE. MSE shall be typically on modules 1 to 3. ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO. ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6. For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)</p>

# **Professional Core (Lab) Courses**

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M. Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M.Tech., Sem I				
<b>Course Code</b>	6PS547				
<b>Course Name</b>	Research Methodology for Power System Engineers				
<b>Desired Requisites:</b>	None				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>		<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Interaction</b>	2 Hrs/week	30	30	40	100
<b>Credits: 2</b>					
<b>Course Objectives</b>					
<b>1</b>	To develop a research orientation among the students and to acquaint them with fundamentals of research methods.				
<b>2</b>	To develop understanding of the basic framework of research process and techniques				
<b>3</b>	To identify various sources of information for literature review and data collection.				
<b>4</b>	To develop an understanding of the ethical dimensions of conducting applied research.				
<b>5</b>	To develop understanding about patent process.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>	<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>		
<b>CO1</b>	Classify various methods to solve research problem.	III	Applying		
<b>CO2</b>	Construct a research problem in respective engineering domain.	III	Applying		
<b>CO3</b>	Investigate various data analysis techniques for a research problem.	IV	Analyzing		
<b>CO4</b>	Identify various Intellectual Property Rights procedures	III	Applying		
<b>Module</b>	<b>Module Contents</b>	<b>Hours</b>			
I	<b>Research Fundamentals</b> What is research, types of research, the process of research, Literature survey and review, Formulation of a research problem.	4			
II	<b>Research Methods</b> Research design- Meaning, Need and Types, Research Design Process, Measurement and scaling techniques, Data Collection - concept, types and methods, Processing and analysis of data, Design of Experiment	5			
III	<b>Analysis Techniques</b> Quantitative Techniques, Sampling fundamentals, Testing of hypothesis using various tests like Multivariate analysis, Use of standard statistical software, Data processing, Preliminary data analysis and interpretation, Uni-variate and bi-variate analysis of data, testing of hypotheses.	5			
IV	<b>Research Communication</b> Writing a conference paper, Journal Paper, Technical report, dissertation/thesis writing. Presentation techniques, software used for report writing such as WORD, Latex etc. Types of journal/conference papers.	4			
V	<b>Intellectual Property Rights</b> Nature of Intellectual Property: Patents, Designs, Trade and Copyright. Process of Patenting and Development: technological research, innovation, patenting, development. International Scenario: International cooperation on Intellectual Property. Procedure for grants of patents, Patenting under PCT.	5			



VI	<b>Patents and Patenting Procedures</b> Patent Rights: Scope of Patent Rights. Licensing and transfer of technology. Patent information and databases. Geographical Indications. New Developments in IPR: Administration of Patent System. New developments in IPR; IPR of Biological Systems, Computer Software etc. Traditional knowledge Case Studies, IPR and IITs	4
<b>Textbooks</b>		
1	C. R. Kothari, Research Methodology, New Age international	
2	Deepak Chopra and Neena Sondhi, Research Methodology: Concepts and cases, Vikas Publishing House, New Delhi	
<b>References</b>		
1	E. Philip and Derek Pugh, How to get a Ph. D. - a handbook for students and their supervisors, open university press	
2	Stuart Melville and Wayne Goddard, Research Methodology: An Introduction for Science & Engineering Students	
<b>Useful Links</b>		
1	NPTEL Lectures	

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1	2		1			
CO2					2	2
CO3				2		
CO4		2				

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

<b>Assessment</b>				
There are three components of lab assessment, LA1, LA2 and Lab ESE. IMP: Lab ESE is a separate head of passing (min 40 %), LA1+LA2 should be min 40%				
Assessment	Based on	Conducted by	Typical Schedule	Marks
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40
Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.				

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M.Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M. Tech., Sem I				
<b>Course Code</b>	6PS545				
<b>Course Name</b>	Digital Protection of Power System Laboratory				
<b>Desired Requisites:</b>	Digital Protection of Power System				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	2 Hrs/ Week	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	--	30	30	40	100
<b>Credits: 1</b>					
<b>Course Objectives</b>					
<b>1</b>	To develop analytical skills of the student and help to evaluate modern relaying practices.				
<b>2</b>	To enable the student to develop protective relaying concepts as well as provide an opportunity for designing relaying hardware and software.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Demonstrate the operation of electromagnetic & digital relays.			III	Applying
<b>CO2</b>	Test digital relays to verify the operating characteristics.			IV & V	Analyzing & Evaluating
<b>CO3</b>	Design hardware and compile programs for simple digital relays, as a group task.			VI	Creating
<b>List of Experiments / Lab Activities/Topics</b>					
Lab activities/performance shall include mini project, presentations, drawings, case study, report writing, site visit, lab experiment, tutorials, assignments, group discussion, programming, and other suitable activities as per nature and requirement of lab course.					
<b>Textbooks</b>					
1	Badri Ram, D.N. Vishwakarma, "Power System Protection and Switchgear", TMH, 2004.				
<b>References</b>					
1	PRDC Relay user manuals				
2	Mi-Power user manuals				
3	A.G. Phadke, J.S. Thorp, "Computer Relaying for Power Systems", Wiley India, II Edi., 2012.				
<b>Useful Links</b>					
1	<a href="https://nptel.ac.in/courses/108/107/108107167/">https://nptel.ac.in/courses/108/107/108107167/</a>				
2	<a href="https://nptel.ac.in/courses/108/105/108105167/">https://nptel.ac.in/courses/108/105/108105167/</a>				

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1			3			
CO2				2	2	
CO3		2				2

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

<b>Assessment</b>				
There are three components of lab assessment, LA1, LA2 and Lab ESE. IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%				
<b>Assessment</b>	<b>Based on</b>	<b>Conducted by</b>	<b>Typical Schedule</b>	<b>Marks</b>
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M.Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M. Tech., Sem I				
<b>Course Code</b>	6PS546				
<b>Course Name</b>	Application of Power Electronics to Power System Laboratory				
<b>Desired Requisites:</b>	Power System Engineering, Power Electronics				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	2 Hrs/ Week	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	--	30	30	40	100
<b>Credits: 1</b>					
<b>Course Objectives</b>					
<b>1</b>	To make students understand concept of FACTs envisages the use of power electronics to improve system operation by fast & reliable control.				
<b>2</b>	To cover concepts of FACTs including the description, principle of working and analysis of various FACTs controllers.				
<b>3</b>	To strengthen the control of FACTs and system interactions.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>	<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>		
<b>CO1</b>	Simulation of various FACTs devices to understand principle and modelling.	II	Understanding		
<b>CO2</b>	Choose the suitable FACTs device/controller for particular application.	III	Applying		
<b>CO3</b>	Analyze the characteristics of FACTs Controllers and effect of location of the controller on Power System.	IV	Analyzing		
<b>List of Experiments / Lab Activities/Topics</b>					
Lab activities/performance shall include mini project, presentations, drawings, case study, report writing, site visit, lab experiment, tutorials, assignments, group discussion, programming, and other suitable activities as per nature and requirement of lab course					
<b>Textbooks</b>					
1	R. Mohan Mathur, Rajiv. K. Varma, "Thyristor - Based Facts Controllers for Electrical Transmission Systems", IEEE press and John Wiley & Sons Inc., 2002				
<b>References</b>					
1	A. T. John, "Flexible AC Transmission System", Institution of Electrical and Electronic Engineers (IEEE), 1999.				
2	Narain G. Hingorani, Laszio. Gyugyl, "Understanding FACTS Concepts and Technology of Flexible AC Transmission System", Standard Publishers, Delhi, 2001.				
<b>Useful Links</b>					
1					

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1			2			
CO2				2		
CO3						2

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

<b>Assessment</b>				
There are three components of lab assessment, LA1, LA2 and Lab ESE. IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%				
<b>Assessment</b>	<b>Based on</b>	<b>Conducted by</b>	<b>Typical Schedule</b>	<b>Marks</b>
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M. Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M. Tech., Sem I				
<b>Course Code</b>	6PS551				
<b>Course Name</b>	Presentation and Technical Report Writing				
<b>Desired Requisites:</b>	None				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>		<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	1 Hrs/ Week	30	30	40	100
<b>Credits: 1</b>					
<b>Course Objectives</b>					
<b>1</b>	To provide an opportunity to student to do work independently on a topic.				
<b>2</b>	To encourage creative thinking process in technical report writing				
<b>3</b>	To enable student for good technical report writing and effective presentations.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>	<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>		
<b>CO1</b>	Demonstrate the characteristics of technical and business writing.	III	Applying		
<b>CO2</b>	Produce documents related to technology and writing in the workplace and will have improved their ability to write clearly, concisely, and accurately.	VI	Creating		
<b>CO3</b>	Use a variety of materials to produce appropriate visual presentation for documents, such as instructions, descriptions, and research reports.	V	Evaluating		
<b>List of Experiments / Lab Activities/Topics</b>					
This course introduces students to the discipline of technical communication. Preparation of visuals to supplement text, workplace communication, descriptions of mechanisms, explanations of processes, and writing reports are the major topics included. This course is designed for students enrolled in technical degree programs for making them industry ready.					
<b>Textbooks</b>					
1	Suitable books based on the contents of the topic.				
<b>References</b>					
1	Suitable books based on the contents of the selected topic and research papers from reputed national and international journals and conferences.				
<b>Useful Links</b>					
1	As per the need of the topic of report and presentation				

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1		3				
CO2		2		1		
CO3		1				2

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

<b>Assessment</b>				
There are three components of lab assessment, LA1, LA2 and Lab ESE. IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%				
<b>Assessment</b>	<b>Based on</b>	<b>Conducted by</b>	<b>Typical Schedule</b>	<b>Marks</b>
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M.Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M. Tech., Sem I				
<b>Course Code</b>	6PS552				
<b>Course Name</b>	Professional Skills 1				
<b>Desired Requisites:</b>	None				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	--	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	1 Hr/ Week	30	30	40	100
<b>Credits: 1</b>					
<b>Course Objectives</b>					
<b>1</b>	To provide a hands-on experience of software in solving complex Electrical Engineering problems.				
<b>2</b>	To enhance the employability of Electrical Engineering student.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>	<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>		
<b>CO1</b>	Use of the software related to Electrical Engineering effectively.	V	Evaluating		
<b>CO2</b>	Develop the solution for Electrical Engineering problem using software.	VI	Creating		
<b>CO3</b>	Explain the process of problem-solving using computing tools.	II	Understanding		
<b>List of Experiments / Lab Activities/Topics</b>					
This course is based on computing as a tool to design and analyze the Electrical Engineering system. In the modern day work environment, the Electrical Engineer should be able to simulate and solve complex problems on computers. The Electrical Engineer must be highly computer literate. The engineer with strong fundamentals and computer software proficiency is highly in demand from industry. Employability of the student can be enhanced by providing software training.					
<b>Textbooks</b>					
1	Suitable books based on the contents of software selected				
<b>References</b>					
1	Suitable books based on the contents of software selected				
<b>Useful Links</b>					
1	As per the need of the software training				



**CO-PO Mapping****Programme Outcomes (PO)**

	1	2	3	4	5	6
CO1	2					
CO2			2			
CO3		3				1

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

**Assessment**

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

# **Professional Elective**

**Walchand College of Engineering, Sangli***(Government Aided Autonomous Institute)***AY 2022-23****Course Information**

<b>Programme</b>	M.Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem I
<b>Course Code</b>	6PS511
<b>Course Name</b>	Professional Elective 1: Power Apparatus Modelling
<b>Desired Requisites:</b>	Power System Engineering, A.C. Machines, Power System Analysis and Stability

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	-	30	20	50	100
<b>Credits: 3</b>					

**Course Objectives**

<b>1</b>	To provide the students the ability to understand the problem of stability of single machine connected to infinite bus and multi machine system.
<b>2</b>	To give the students a sound mathematical approach towards modelling of various approach used in power system.

**Course Outcomes (CO) with Bloom's Taxonomy Level**

At the end of the course, the students will be able to,

CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
<b>CO1</b>	Construct models of apparatus in power system.	III	Applying
<b>CO2</b>	Analyze models for stability of power systems.	IV	Analyzing
<b>CO3</b>	Recommend solutions to the problem of power system stability and control.	V	Evaluating

Module	Module Contents	Hours
I	<b>Introduction to Power System Stability Problem</b> Classification of stability, resolution of stability problem by classical method, transient stability of multi-machine system.	6
II	<b>Modeling of Synchronous machine</b> Physical description, mathematical description of synchronous machine, dq0 transformation, per unit representation, equivalent circuits for direct and quadrature axis.	6
III	<b>Excitation System</b> Elements of excitation system, types of excitation system, necessity of stabilizing circuits IEEE excitation systems.	6
IV	<b>Prime Movers and Energy supply Systems</b> Turbines and governing systems, modeling of steam turbines, steam turbine controls, steam turbine off-frequency capability.	6
V	<b>Dynamic modeling of hydro turbine and governors</b> Hydraulic turbine transfer function, governors for hydraulic turbines, detailed hydraulic system model, guidelines for modeling hydraulic turbines	6
VI	<b>Load modeling for stability studies</b> Basic load modeling concepts, static load models, dynamic load models, modeling of induction motor, per unit representation, representation in stability studies	6

**Textbooks**

1	P. Kundur, <i>Power System, Stability and Control</i> , Tata McGraw Hill, New Delhi, 1994.
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<b>References</b>	
1	K. R. Padiyar, “ <i>Power System Dynamic, Stability &amp; Control</i> ”, B.S. Publication, 2008.
2	Peter W.Sauer, M.A. Pai, “ <i>Power System Dynamics and Stability</i> ”, Person Education Asia, 1998.
<b>Useful Links</b>	

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1			3			
CO2				3		
CO3						2
<p>The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  Each CO of the course must map to at least one PO.</p>						

<b>Assessment</b>
<p>The assessment is based on MSE, ISE and ESE.  MSE shall be typically on modules 1 to 3.  ISE shall be taken throughout the semester in the form of teacher’s assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.  ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.  For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)</p>

## Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

**AY 2022-23**

### Course Information

<b>Programme</b>	M. Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem I
<b>Course Code</b>	6PS512
<b>Course Name</b>	Professional Elective 1: DSP Application to Power System
<b>Desired Requisites:</b>	Signals and Systems

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	-	30	20	50	100
<b>Credits: 3</b>					

### Course Objectives

<b>1</b>	To provide a mathematical introduction to the theory and applications of orthogonal wavelets and their use in analyzing functions and function spaces.
<b>2</b>	It includes a brief survey of Fourier series representation of functions, Fourier transform and the Fast Fourier Transform (FFT) before proceeding to the Haar wavelet system, multi resolution analysis, decomposition and reconstruction of functions, Daubechies wavelet construction, and other wavelet systems.
<b>3</b>	It aims at imparting skills to develop wavelet-based algorithms for applications in the area of Power Systems.

### Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,			
CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
<b>CO1</b>	Explain the basic concepts and terminology that are used in the Fourier Techniques, wavelets Transforms and Time frequency analysis.	II	Understanding
<b>CO2</b>	Calculate filter bank coefficients and Apply the concepts of CWT, STFT and DWT for signal analysis.	III	Applying
<b>CO3</b>	Construct perfect reconstruction wavelet filter banks for a particular application and justify why wavelets provide the right tool.	IV	Analyzing

Module	Module Contents	Hours
I	<b>Fundamentals of Linear Algebra</b> Vector spaces, Bases, Orthogonality, Orth normality, Projection, Functions and function Spaces, Orthogonal functions, Orthonormal functions, Orthogonal basis functions.	4
II	<b>Signal Representation in Fourier Domain</b> Fourier series, Orthogonality, Orth normality and the method of finding the Fourier coefficients Complex Fourier series, Orthogonality of complex exponential bases, Mathematical preliminaries for continuous and discrete Fourier transform, limitations of Fourier domain signal processing, Review of Nyquist theorem., Review of Z transform, Application of Fourier family transforms in power systems.	6

III	<p><b>Discrete Wavelet Transform</b></p> <p><b>Introduction to Wavelet Transform:</b> The origins of wavelets, Wavelets and other wavelet like transforms, History of wavelet from Morlet to Daubechies via Mallat, Different communities and family of wavelets, Different families of wavelets within wavelet communities</p> <p><b>Discrete wavelet transforms:</b> Introduction, Haar Scaling Functions and Function Spaces, Translation and Scaling, Orthogonality of Translates, Function Space <math>V_0</math>, Finer Haar Scaling Functions, Nested Spaces Haar Wavelet Function, Scaled Haar Wavelet Functions, Orthogonality of <math>\varphi(t)</math> and <math>\psi(t)</math>, Normalization of Haar Bases at Different Scales, Standardizing the Notations, Refinement Relation with Respect to Normalized Bases, Support of a Wavelet System, Triangle Scaling Function, Daubechies Wavelets.</p>	8
IV	<p><b>Discrete Wavelet Transform and Relation to Filter Banks</b></p> <p>Signal decomposition (Analysis), Relation with filter banks, Frequency response, Signal reconstruction: Synthesis from coarse scale to fine scale, Up sampling and filtering, Perfect reconstruction filters, QMF conditions, Computing initial <math>s_{j+1}</math> coefficient, Concepts of Multi-Resolution Analysis (MRA) and Multi-rate signal processing, Applications of DWT in power systems.</p>	8
V	<p><b>Short Time Fourier Transform (STFT) and Continuous Wavelet Transform (CWT)</b></p> <p><b>Short Time Fourier Transform:</b> Signal representation with continuous and discrete STFT, concept of time-frequency resolution, Resolution problem associated with STFT, Heisenberg's Uncertainty principle and time frequency tiling, why wavelet transform?</p> <p><b>Continuous Wavelet Transform:</b> Wavelet transform-A first level introduction, Continuous time-frequency representation of signals, Properties of wavelets used in continuous wavelet transform, Continuous versus discrete wavelet transform.</p>	6
VI	<p><b>Designing Orthogonal Wavelet Systems-A Direct Approach</b></p> <p>Refinement relation for orthogonal wavelet systems, Restrictions on filter coefficients, Condition-1: Unit area under scaling function, Condition-2: Orth normality of translates of scaling functions, Condition-3: Orth normality of scaling and wavelet functions, Condition-4: Approximation conditions (Smoothness conditions), Designing Daubechies orthogonal wavelet system coefficients, Constraints for Daubechies' 6 tap scaling function.</p>	6
<b>Textbooks</b>		
1	K P Soman, Ramachandran, Resmi, <i>"Insights into wavelets from theory to practice"</i> , Prentice Hall, New Delhi,	
2	A.N. Akansu and R.A. Haddad, <i>"Multiresolution signal Decomposition: Transforms, Subbands and Wavelets"</i> , Academic Press, Oranld, Florida, 1992.	
3	John G. Proakis, Dimitris G. Manolakis, <i>"Digital Signal Processing"</i> , Pearson Prentice Hall,2007.	
<b>References</b>		
1	C. Sidney Burrus, Ramesh A. Gopinath, HaitaoGuo, <i>"Introduction to Wavelets and Wavelet Transform"</i> s, A Primer PH International Editions, 1998.	
2	Raghuveer M. Rao, Ajit S. Bopardikar, <i>"Wavelet Transforms - Introduction to Theory and Application's"</i> , Addison Wesley Pearson Education Asia, 2000.	
3	IEEE Transaction Papers.	
<b>Useful Links</b>		
1	<a href="https://nptel.ac.in/courses/117/101/117101001/">https://nptel.ac.in/courses/117/101/117101001/</a>	

**CO-PO Mapping****Programme Outcomes (PO)**

	1	2	3	4	5	6
CO1			2			
CO2				2		
CO3						3

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

**Assessment**

The assessment is based on MSE, ISE and ESE.

MSE shall be typically on modules 1 to 3.

ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.

For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>		M. Tech. (Power System Engineering)			
<b>Class, Semester</b>		First Year M. Tech., Sem I			
<b>Course Code</b>		6PS513			
<b>Course Name</b>		Professional Elective 2: Grid Integration of Renewable Energy			
<b>Desired Requisites:</b>		Power Electronics, Renewable Energy			
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	-	30	20	50	100
<b>Credits: 3</b>					
<b>Course Objectives</b>					
<b>1</b>	To make the students conversant with configurations of renewable energy grid integration.				
<b>2</b>	To provide the advance knowledge about voltage-sourced converters & their control.				
<b>3</b>	To make the students aware of research avenues in the field of renewable grid integration along with DC micro-grid concepts.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Summarize two level voltage source converters in various reference frame.			II	Understanding
<b>CO2</b>	Apply various voltage source converters and their control.			III	Applying
<b>CO3</b>	Analyze grid synchronization techniques and DC micro-grid.			IV	Analyzing
<b>Module</b>	<b>Module Contents</b>				<b>Hours</b>
I	<b>Overview of Renewable Energy</b> Status & trends of renewable energy sources, solar fundamentals, electrical characteristics of PV, stand-alone grid connected PV configurations, wind energy assessment, fixed & variable speed turbines with reduced & full capacity converters.				7
II	<b>Two level, three phase voltage-sourced converter</b> Introduction. Two level voltage sourced-converter: structure, principle of operation & power loss. Average model of two level VSC, model in $\alpha\beta$ -frame, model & control in dq frame.				6
III	<b>Three level, three phase, Neutral Point Clamped voltage-sourced converter</b> Introduction, Three level half bridge NPC, PWM scheme for three level half bridge NPC, switched model & average model for three level half bridge NPC, three level NPC: circuit structure, principle of operation. Three level NPC with impressed dc side voltage.				6
IV	<b>Grid Imposed frequency VSC system: control in <math>\alpha\beta</math>-frame &amp; dq-frame</b> Introduction, structure of grid-imposed frequency VSC system, real & reactive-power controller, Dynamic model & current mode control for real-/reactive power controller in $\alpha\beta$ -frame & dq frame, Phase locked Loop.				6
V	<b>Grid Synchronization</b> Grid synchronization techniques for single-phase systems, grid synchronization using the Fourier analysis, grid synchronization using A phase-locked loop, PLL Based on a T/4 transport delay, PLL based on the Hilbert transform.				6



VI	<b>DC Micro-grid</b> Introduction, DC micro-grid system overview, Operation and control of DC micro-grids, DC micro-grid system protection, Application of DC micro-grids to future smart grids.	5
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#### Textbooks

1	Amirnaser Yazdani and Reza Iravani, “ <i>Voltage-sourced converters in power systems_ modeling, control, and applications</i> ” IEEE Press John Wiley, 2010.
2	Remus Teodorescu, Marco Liserre, and Pedro Rodriguez, “ <i>Grid Converters for Photovoltaic and Wind Power Systems</i> ”, John Wiley & Sons, Ltd, 2011.

#### References

1	Antonio Moreno-Munoz, “ <i>Large Scale Grid Integration of Renewable Energy Sources</i> ”, The Institution of Engineering and Technology, 2017.
2	Math J. Bollen and Fainan Hassan, “ <i>Integration of Distributed Generation in the Power System</i> ”, IEEE Press, 2011.

#### Useful Links

1	<a href="http://nptel.ac.in/downloads">http://nptel.ac.in/downloads</a>
2	<a href="http://www.nptelvideos.in">http://www.nptelvideos.in</a>
3	<a href="https://ocw.mit.edu/courses/electrical-engineering">https://ocw.mit.edu/courses/electrical-engineering</a>

#### CO-PO Mapping

##### Programme Outcomes (PO)

	1	2	3	4	5	6
CO1			3			
CO2				2		
CO3						2

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High

Each CO of the course must map to at least one PO.

#### Assessment

The assessment is based on MSE, ISE and ESE.

MSE shall be typically on modules 1 to 3.

ISE shall be taken throughout the semester in the form of teacher’s assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.

For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M. Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M.Tech., Sem I				
<b>Course Code</b>	6PS514				
<b>Course Name</b>	Professional Elective 2: Neural Network and fuzzy Application to Power System				
<b>Desired Requisites:</b>	Power System				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	-	30	20	50	100
<b>Credits: 3</b>					
<b>Course Objectives</b>					
<b>1</b>	To make the student conversant with basic knowledge of Neural Network.				
<b>2</b>	To make the student conversant with design and programming knowledge for power system operation and control.				
<b>3</b>	To make the student conversant with basic knowledge of fuzzy system and fuzzy applications.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Explain the basic knowledge of Neural Network			II	Understanding
<b>CO2</b>	Apply the Neural network and fuzzy knowledge about different neural networks, their architecture and training algorithm to solve power system problems.			III	Applying
<b>CO3</b>	Study the different applications of neural networks and fuzzy logic.			IV	Analyzing
<b>Module</b>	<b>Module Contents</b>				<b>Hours</b>
I	<b>Introduction to Neural Networks</b> Introduction, Organization of the Brain, Biological Neuron, Biological and Artificial Neuron Models, Historical Developments, Neuron Model, McCulloch and Pitts models of neuron, ANN terminologies, weights, sigmoidal functions, Bias.				6
II	<b>Essentials of Neural Networks</b> Types of Neuron Activation Function, Neural networks architectures, Linearly separable and linearly non separable systems and their examples, Learning Strategy (Supervised, Unsupervised, Reinforcement), Learning Rules, Hebbian learning rule, Perceptron learning rule etc.				6
III	<b>Feed Forward Neural Networks</b> Introduction, single layer Perceptron Models, architecture, Limitations of the Perceptron Model, Applications, Back Propagation Network, architecture, Multilayer Feed Forward Neural Networks. Use of ANN MATLAB tools for programming.				6
IV	<b>Fuzzy Systems</b> Basic Fuzzy logic theory, history, operation of Fuzzy Logic, Fuzzy relation and extension principle, Fuzzy membership functions and linguistic variables, Mamdani and sugenos models. Use of MATLAB tools of fuzzy logic.				6

V	<b>Application of Neural Network and fuzzy to power system operation and control problems</b> Use of MATLAB tools of ANN and fuzzy logic for power system applications. Case studies such as load fore-casting, optimal power flow, control applications in FACTS devices, etc.	6
VI	<b>Application of Neural Network and fuzzy to recent power system protection problems</b> Use of MATLAB tools of ANN and fuzzy logic for protection applications. Case studies such as fault analysis, fault detection, fault classification, fault location, etc.	6

#### Textbooks

1	S. N. Sivanandam, “ <i>Introduction to Neural Networks using MATLAB 6</i> ”, Tata McGraw hill education, 2006.
2	Hagan, Demuth, Mark Beale, “ <i>Neural Network Design</i> ”, Cengage Learning India Private Limited, 2011.

#### References

1	Stamatios V. Kartalopoulos, “Understanding neural networks and fuzzy logic basic concepts and applications”, Prentice Hall of India (P) Ltd, New Delhi, 2000.
2	J.M. Zurada, “Introduction to artificial neural systems”, Jaico Publishers, 1992.
3	Timothy Ross, “Fuzzy Logic with Engineering Applications”, Tata McGraw Hill Publication, 1993
4	George J. Klir and Bo Yuan, “Fuzzy Sets and Fuzzy Logic”, PHI Learning Private Limited, 1995.
5	Research Papers.

#### Useful Links

1	<a href="https://onlinecourses.nptel.ac.in/noc21_ge07/preview">https://onlinecourses.nptel.ac.in/noc21_ge07/preview</a>
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#### CO-PO Mapping

##### Programme Outcomes (PO)

	1	2	3	4	5	6
CO1						1
CO2				3		
CO3				2		

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

#### Assessment

The assessment is based on MSE, ISE and ESE.

MSE shall be typically on modules 1 to 3.

ISE shall be taken throughout the semester in the form of teacher’s assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.

For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)

# **Semester- II**

# **Professional Core (Theory)**

# **Courses**

## Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

**AY 2022-23**

### Course Information

<b>Programme</b>	M. Tech (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem II
<b>Course Code</b>	6PS521
<b>Course Name</b>	Power Quality in Distribution Systems
<b>Desired Requisites:</b>	Power Systems, Power Electronics

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	--	30	20	50	100
<b>Credits: 3</b>					

### Course Objectives

<b>1</b>	To make the students to understand basic knowledge of causes, consequences and solutions of power quality problems that affect the operation of computerized processes and electronic systems.
<b>2</b>	To provide a theoretical background to correctly approach the problem of reactive, harmonic and unbalance compensation.
<b>3</b>	To understand and apply the power theories for compensation problems.

### Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,			
CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	State and explain the basic concepts of Power Quality disturbances, reactive power compensation, voltage regulation, power definitions and other figures of merit under distorted, operation and modelling of series and shunt compensators.	I & II	Remembering & Understanding
CO2	Apply the theory and algorithms to realize reference current generation, reactive power compensation, voltage regulation and harmonic compensation.	III	Applying
CO3	Analyze theories of load compensation, reference generation, figures of merits and power definitions, Standards applicable to Power Quality.	IV	Analyzing

Module	Module Contents	Hours
I	<p><b>Introduction to Power quality</b>  <b>Power Quality:</b> Introduction, State of the Art on Power Quality, Classification of Power Quality Problems, Causes of Power Quality Problems, Effects of Power Quality Problems on Users, Classification of Mitigation Techniques for Power Quality Problems.</p> <p><b>Power Quality Standards and Monitoring:</b> Introduction, State of the Art on Power Quality Standards and Monitoring, Power Quality Terminologies, Power Quality Definitions, Power Quality Standards, Power Quality Monitoring, Numerical Examples.</p>	6
II	<p><b>Power Definitions in Single Phase and Three phase Circuits</b>            Definitions of various powers, power factor and other figures of merit under balanced, unbalanced and non-sinusoidal conditions applicable to single phase circuits. Definitions of various powers, power factor and other figures of merit under balanced, unbalanced and non-sinusoidal conditions. IEEE 1459 power definitions applicable to three phase circuits</p>	6

III	<b>Theories of Load compensation</b> Introduction, State of the Art on Passive Shunt and Series Compensators, Classification of Passive Shunt and Series Compensators, Principle of Operation of Passive Shunt and Series Compensators, Analysis and Design of Passive Shunt Compensators, Modelling, Simulation, and Performance of Passive Shunt and Series Compensators, Numerical Examples	6
IV	<b>Active Shunt Compensation</b> Introduction, State of the Art on DSTATCOMs, Classification of DSTATCOMs, Principle of Operation and Control of DSTATCOMs, Analysis and Design of DSTATCOMs, Modelling, Simulation, and Performance of DSTATCOMs, Numerical Examples.	6
V	<b>Active Series Compensation</b> Introduction, State of the Art on Active Series Compensators, Classification of Active Series Compensators, Principle of Operation and Control of Active Series Compensators, Analysis and Design of Active Series Compensators, Modelling, Simulation, and Performance of Active Series Compensators, Numerical Examples.	6
VI	<b>Unified Power Quality Compensators</b> Introduction, State of the Art on Unified Power Quality Compensators, Classification of Unified Power Quality Compensators, Principle of Operation and Control of Unified Power Quality Compensators, Analysis and Design of Unified Power Quality Compensators, Modelling, Simulation, and Performance of UPQCs, Numerical Examples.	6

#### Textbooks

1	Dr. Mahesh Kumar, IIT Chennai, " <i>Power Quality in Distribution Systems</i> ".
2	Bhim Singh, Ambrish Chandra, Kamal Al-Haddad, " <i>Power Quality Problems and Mitigation Techniques</i> ", Wiley, 2015.

#### References

1	Roger C. Dugan, Mark F. McGranaghan, Surya Santoso, H. Wayne Beaty, " <i>Electrical Power Systems Quality</i> ", Mc-Graw Hill, Edition II, 1996.
2	Angelo Baggingi, " <i>Handbook on Power Quality</i> ", John Wiley & Sons, New Jersey, USA, 2008

#### Useful Links

1	<a href="https://nptel.ac.in/courses/108/106/108106025/">https://nptel.ac.in/courses/108/106/108106025/</a>
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#### CO-PO Mapping

##### Programme Outcomes (PO)

	1	2	3	4	5	6
CO1			1			
CO2	2					
CO3				1		2

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

#### Assessment

The assessment is based on MSE, ISE and ESE.  
MSE shall be typically on modules 1 to 3.  
ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.  
ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.  
For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)

## Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

**AY 2022-23**

### Course Information

<b>Programme</b>	M.Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M.Tech., Sem II
<b>Course Code</b>	6PS522
<b>Course Name</b>	PLC and Embedded Systems
<b>Desired Requisites:</b>	Instrumentation Techniques, Electrical Measurements, Microcontroller and Applications

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	--	30	20	50	100
<b>Credits: 3</b>					

### Course Objectives

<b>1</b>	To exploit the PLC and Embedded Control for industrial automation.
<b>2</b>	To developing programs using ladder logic for industrial automation.
<b>3</b>	To analyze the performance of automation systems employing PLC and Embedded Control.

### Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
<b>CO1</b>	Interpret features of PLC and Embedded Control Systems used for Industrial Automation.	III	Applying
<b>CO2</b>	Use ladder logic programming technique for various PLC applications.	III	Applying
<b>CO3</b>	Evaluate the performance of PLC network configurations, PLC functions used for different application	V	Evaluating

Module	Module Contents	Hours
I	<b>Introduction to PLC</b> Introduction, Advantages, Disadvantages, Parts of PLC, PLC Input module, PLC Output Module, PLC Architecture, PLC Operation, PLC as a computer, PLC memory and interfacing, Power Supply for PLC	6
II	<b>PLC programming</b> Ladder Logic Symbols, Latching and Unlatching of PLC, Programming on/ off inputs to produce on/off outputs, relation of digital gate logic to contact / coil logic, creating ladder diagrams from process control description.	6
III	<b>PLC Timer and Counter Functions</b> PLC timer functions, Types of PLC timers, Programming of Non-retentive timers for various applications, Programming of ON timers, OFF timers, PLC counter functions, Programming of UP, DOWN counters, Case studies related to Industrial Automations	6
IV	<b>PLC Arithmetic, Comparison and Branch functions</b> PLC Arithmetic functions, PLC comparison functions, Conversion functions, Master control relay functions, PLC jump functions, Jump with return and Jump with No return functions, Programs related to Arithmetic, Comparison and Branch functions	6
V	<b>Advanced PLC functions</b> Data move system, Data handling functions, Digital bit functions and applications, Sequencer functions, Analog PLC operations, PID control of continuous process, PID modules & tuning, Typical PID functions	6

VI	<b>PLC Networking</b> Networking of PLCs, Levels of Industrial Control, Types of Networking, Network Communications, Cell control by PLC Networks, Factors to consider in selecting a PLC	6
<b>Textbooks</b>		
1	John W. Webb, Ronald A. Reis, "Programmable logic controllers, principles & applications", PHI publication, Eastern Economic Edition, 1994.	
<b>References</b>		
1	John R. Hackworth and Peterson, " <i>PLC controllers programming methods and applications</i> ", PHI, 2004.	
2	Gary dunning, " <i>Introduction to PLC</i> , Thomson learning", Edition III, 2006	
3	William H. Bolton, " <i>Programmable logic controllers</i> ", Newnes, Edition VI, 2006.	
<b>Useful Links</b>		

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1			2			
CO2			2			
CO3				2		
The strength of mapping is to be written as 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO.						

<b>Assessment</b>
<p>The assessment is based on MSE, ISE and ESE.</p> <p>MSE shall be typically on modules 1 to 3.</p> <p>ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.</p> <p>ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.</p> <p>For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)</p>



# **Professional Core (Lab) Courses**

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M. Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M. Tech., Sem II				
<b>Course Code</b>	6PS591				
<b>Course Name</b>	Power Quality in Distribution Systems Laboratory				
<b>Desired Requisites:</b>	Power Systems, Power Electronics				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	2 Hrs/ Week	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	--	30	30	40	100
<b>Credits: 1</b>					
<b>Course Objectives</b>					
<b>1</b>	To educate the students with the practical aspects of Power Quality issues.				
<b>2</b>	To develops the critical thinking in solving power quality problems with contemporary Power Quality Theories.				
<b>3</b>	To enhance research skills of students to Power Quality issues.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Calculate power components and other figures of merit under distorted conditions.			III	Applying
<b>CO2</b>	Analyze Power Quality Problems and provide suitable remedy.			IV	Analyzing
<b>CO3</b>	Evaluate theories of load compensation, reference generation using suitable simulation tool.			V	Evaluating
<b>List of Experiments / Lab Activities/Topics</b>					
Lab activities/Lab performance shall include mini-project, presentations, drawings, case studies, report writing, site visit, lab experiment, tutorials, assignments, group discussion, programming and other suitable activities, as per the nature and requirement of the lab course.					
<b>Textbooks</b>					
1	Dr. Mahesh Kumar, IIT Chennai, " <i>Power Quality in Distribution System</i> ".				
2	Bhim Singh, Ambrish Chandra, Kamal Al-Haddad, " <i>Power Quality Problems and Mitigation Techniques</i> ", Wiley, 2015.				
<b>References</b>					
1	Roger C. Dugan, Mark F. McGranaghan, Surya Santoso, H. Wayne Beaty, " <i>Electrical Power Systems Quality</i> ", Mc-Graw Hill, Edition II, 1996.				
2	Angelo Bagginì, " <i>Handbook on Power Quality</i> ", John Wiley & Sons, New Jersey, USA, 2008				
<b>Useful Links</b>					
1					

**CO-PO Mapping****Programme Outcomes (PO)**

	1	2	3	4	5	6
CO1			2			
CO2				2		
CO3						3

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

**Assessment**

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

<b>Assessment</b>	<b>Based on</b>	<b>Conducted by</b>	<b>Typical Schedule</b>	<b>Marks</b>
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M. Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M.Tech., Sem II				
<b>Course Code</b>	6PS592				
<b>Course Name</b>	PLC and Embedded Systems Laboratory				
<b>Desired Requisites:</b>	Instrumentation Techniques, Electrical Measurements, Microcontroller and Applications				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	2 Hrs/ Week	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	--	30	30	40	100
<b>Credits: 1</b>					
<b>Course Objectives</b>					
<b>1</b>	To develop programming skills using PLC for Industrial Automation				
<b>2</b>	To introduce the use of PLC for solving real world problems.				
<b>3</b>	To use PLC for control applications in electrical engineering				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Execute experiments based on PLC and SCADA systems			III	Applying
<b>CO2</b>	Construct basic control systems using PLC and SCADA.			IV	Analyzing
<b>CO3</b>	Design ladder logic programs for various PLC applications.			V	Creating
<b>List of Experiments / Lab Activities/Topics</b>					
Lab activities/Lab performance shall include mini-project, presentations, drawings, case studies, report writing, site visit, lab experiment, tutorials, assignments, group discussion, programming and other suitable activities, as per the nature and requirement of the lab course.					
<b>Textbooks</b>					
1	John W. Webb, Ronald A. Reis, "Programmable logic controllers, principles & applications", PHI publication, Eastern Economic Edition, 1994.				
<b>References</b>					
1	John R. Hackworth and Peterson, "PLC controllers programming methods and applications", PHI, 2004.				
2	Gary dunning, "Introduction to PLC", Thomson learning, Edition III, 2006				
3	William H. Bolton, "Programmable logic controllers", Newnes, Edition VI, 2006.				
<b>Useful Links</b>					
1	Computer Usage / Lab Tool: MATLAB/TLS/Power world/MiPower Simulator				

**CO-PO Mapping****Programme Outcomes (PO)**

	1	2	3	4	5	6
CO1			2			
CO2				2		
CO3				2		

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

**Assessment**

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M.Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M. Tech., Sem II				
<b>Course Code</b>	6PS593				
<b>Course Name</b>	Pre-dissertation Work and Seminar				
<b>Desired Requisites:</b>	-				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	4 Hr/ Week	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	--	30	30	40	100
		<b>Credits: 2</b>			
<b>Course Objectives</b>					
<b>1</b>	To understand industrial problems.				
<b>2</b>	To suggest engineering solutions to the defined problem.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Chose, Formulate a clear problem.			III	Applying
<b>CO2</b>	Select and apply appropriate engineering methods and tools for solving the problem.			VI	Creating
<b>CO3</b>	Develop the project and its results following an established project methodology.			V	Evaluating
<b>CO4</b>	Present the project results.			IV	Analyzing
<b>List of Experiments / Lab Activities/Topics</b>					
<p>Pre-Dissertation seminar will involve the selection of appropriate real time industry problem by understanding the working of particular industry application. Formulate the problem, select design and methodology to find the solution. Construct an electrical system by using appropriate hardware software tools. Each student should conceive, design and develop the idea leading to a project/product. The student should submit a soft bound report at the end of the semester. The final product as a result of Industry project should be demonstrated in phases at the time of examination.</p> <p>This will help student to understand structured management in industry, sustainable development, with consideration to both scientific and ethical aspects and its presentation with technical report.</p>					
<b>Textbooks</b>					
1	To be used based on selected project				
<b>References</b>					
1	Industry 4.0 : fourth Industrial Revolution guide to Industry 4.0				
<b>Useful Links</b>					
1					

**CO-PO Mapping****Programme Outcomes (PO)**

	1	2	3	4	5	6
CO1	3	2				
CO2				2		2
CO3			2			
CO4		2				

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

**Assessment**

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

# **Professional Elective**



**Walchand College of Engineering, Sangli  
(Government Aided Autonomous Institute)**

**AY 2022-23**

**Course Information**

<b>Programme</b>	M. Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem II
<b>Course Code</b>	6PS531
<b>Course Name</b>	Professional Elective 3: Modern Power Electronics
<b>Desired Requisites:</b>	Power Electronics

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	--	30	20	50	100
<b>Credits: 3</b>					

**Course Objectives**

<b>1</b>	It is aimed to impart skills of analysis for different types of advanced converters and shunt active power filters.
<b>2</b>	Make the students acquainted with control strategies of different types of advanced converters and shunt active power filters.
<b>3</b>	To make aware of research avenues in the field of power electronics.

**Course Outcomes (CO) with Bloom's Taxonomy Level**

At the end of the course, the students will be able to,

CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
<b>CO1</b>	Interpret configuration and working of various Power Electronic converters.	III	Applying
<b>CO2</b>	Analyze various Power Electronic converters and systems.	IV	Analyzing
<b>CO3</b>	Evaluate various power electronic systems using power electronic converters.	V	Evaluating

Module	Module Contents	Hours
I	<b>PWM rectifiers</b> Advantages & disadvantages of three phase thyristor converter, Single phase and three phase VSI PWM converters working, types, Control of PWM rectifiers, analysis and application. Three phase CSI PWM converter, control and applications.	5
II	<b>Multilevel inverters</b> Three phase two level Voltage source inverter, various PWM methods, Multilevel Voltage source inverter, Types: Diode clamp multilevel inverter, flying capacitor multilevel inverter, cascaded multilevel inverter, applications of multilevel inverters, comparison of multilevel inverter. Control method: Multiple carrier PWM for MLI	5
III	<b>Resonant pulse inverters</b> Series resonant inverter with unidirectional and bi-directional switches, parallel resonant inverters, voltage control of resonant inverters, zero current and zero voltage switching resonant converters, two-quadrant ZVS resonant converters, resonant DC link inverters and control technique.	5
IV	<b>Photovoltaic Inverters</b> Photovoltaic Inverters structures derived from H bridge topology such as H5 inverter, Heric inverter, REFU inverter, full bridge inverter with DC bypass, inverter structures derived from NPC topology such as neutral point clamped half bridge inverter, conergy NPC inverter, three phase PV inverter.	5

V	<b>Matrix Converters and Z source inverters</b> Topology, working and control methods of Matrix converters, Various circuit topologies and control of Z source inverter, Application of Z source in induction motor control.	4
VI	<b>Active power filters</b> Power Quality Issues due to power Electronics, Introduction to active power filter, types of active power filters overall control of shunt active power filter, control of shunt active filter based on SRF theory. Control of shunt active filter based on instantaneous power theory. harmonic compensation & reactive power compensation.	4

#### Textbooks

1	M. H. Rashid, “ <i>Power Electronics: circuits devices and applications</i> ”, Pearson Education, Third edition.
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#### References

1	B. K. Bose, “ <i>Modern Power Electronics and AC drives</i> ”, PHIPL, New Delhi.
2	M. B. Patil, V. Ramayanan and V. T. Ranganathan, “ <i>Simulation of Power Electronics circuits</i> ”, Narosa publication.
3	Remus Teodorescu, Marco Liserre and Pedro Rodrigues, “ <i>Grid- Converters for Photovoltaic and Wind Power Converters</i> ”, A John Wiley and sons Ltd., first edition 2011.
4	IEEE Transaction papers.

#### Useful Links

1	NPTEL lectures on Advanced Power Electronics
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#### CO-PO Mapping

##### Programme Outcomes (PO)

	1	2	3	4	5	6
CO1			1			
CO2				1		
CO3				2		1

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

#### Assessment

The assessment is based on MSE, ISE and ESE.  
MSE shall be typically on modules 1 to 3.  
ISE shall be taken throughout the semester in the form of teacher’s assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.  
ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.  
For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)

## Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

**AY 2022-23**

### Course Information

<b>Programme</b>	M. Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem II
<b>Course Code</b>	6PS532
<b>Course Name</b>	Professional Elective 3: EHVAC
<b>Desired Requisites:</b>	Power System

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	-	30	20	50	100
<b>Credits: 3</b>					

### Course Objectives

<b>1</b>	To understand parameters of EHVAC line.
<b>2</b>	To develop a skill to design and analyze EHVAC line.
<b>3</b>	To develop a skill to understand power frequency over voltages developed in EHVAC line.
<b>4</b>	To develop a skill to understand insulation coordination based on lightning.

### Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Outline parameters of EHVAC line and develop skills to design and analyze EHVAC line.	II	Understanding
CO2	Examine power frequency over voltages developed in EHVAC line.	III	Applying
CO3	Explain insulation coordination based on lightning.	IV	Analyzing

Module	Module Contents	Hours
I	<p><b>Introduction, Calculation of Line and Ground Parameters, Voltage Gradients of Conductor and Corona Effects.</b></p> <p><b>A.</b> Introduction: Engineering aspects and growth of EHVAC transmission line trends and preliminaries, power transferability, transient stability limit and surge impedance loading.</p> <p><b>B.</b> Calculation of Line and Ground Parameters: Resistance, power loss, temperature rise, properties of bundled conductors, inductances, and capacitances, calculation of sequence inductance and capacitance line parameters of modes of propagations, resistance and inductance of ground return.</p> <p><b>C.</b> Voltage Gradients of Conductor: Charge potential relations for multi-conductor lines, surface voltage gradients on conductors, distribution of voltage gradient on sub conductors of bundle.</p> <p><b>D.</b> Corona Effects: I<sup>2</sup>R and corona loss, corona loss formulae, charge voltage diagram with corona. Attenuation of traveling waves due to corona loss Audible noise; corona pulses; their generation and properties, limits for radio interface fields.</p>	6
II	<p><b>Theory of Traveling Waves and Standing Waves</b></p> <p>Waves at power frequency, differential equations and solutions for general case, standing waves and natural frequencies, open ended line; double exponential response, response to sinusoidal excitation, line energization with trapped charge voltage, reflection and refraction of traveling waves.</p>	6

III	<b>Lightning and Lightning Protection</b> Lightning strokes to lines, their mechanism, general principals of lightning protection problem, tower footing resistance, lightning arresters and protective characteristics, different arresters and their characteristics.	4
IV	<b>Over Voltage in EHV Systems Covered by Switching Operations</b> Over voltages their types, recovery voltage and circuit breaker, Ferro resonance over voltages calculation of switching surges single phase equivalents.	4
V	<b>Power Frequency Voltage Control and Over Voltages</b> Generalized constants, charging current, power circle diagram and its use, voltage control shunt and series compensation, sub synchronous resonance in series capacitor compensated lines and static reactive compensating systems.	4
VI	<b>Insulation Coordination</b> Insulation coordination, Insulation levels, voltage withstand levels of protected equipment's and insulation coordination based on lightning, Design of EHVAC lines.	4
<b>Textbooks</b>		
1	Rakosh Das Begamudre, "EHVAC Transmission Engineering", Wiley Eastern Limited, 3rd Edition 2008.	
<b>References</b>		
1	Twian Gonen, " <i>EHVAC and HVDC Transmission System Engineering – Analysis and Design</i> " John Wiley and Sons 1988.	
2	EHVAC and HVDC Transmission Engineering & Practice: S.V. Rao	
3	Twian Gonen, " <i>Electric Power Transmission System Engineering-Analysis and Design</i> ", John Wiley and Sons 1988.	
<b>Useful Links</b>		
1	NPTEL Lectures	

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1	3					
CO2				3		
CO3						2
The strength of mapping is to be written as 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO.						

<b>Assessment</b>
<p>The assessment is based on MSE, ISE and ESE.</p> <p>MSE shall be typically on modules 1 to 3.</p> <p>ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.</p> <p>ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.</p> <p>For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)</p>

# **Professional Elective (Lab) Courses**

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>		M. Tech. (Power System Engineering)			
<b>Class, Semester</b>		First Year M. Tech., Sem II			
<b>Course Code</b>		6PS571			
<b>Course Name</b>		Professional Elective 3: Modern Power Electronics Laboratory			
<b>Desired Requisites:</b>		Power Electronics			
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	2 Hrs/ Week	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	--	30	30	40	100
		<b>Credits: 1</b>			
<b>Course Objectives</b>					
<b>1</b>	It is aimed to impart skills of analysis for different types of advanced converters and shunt active power filters.				
<b>2</b>	Make the students acquainted with control strategies of different types of advanced converters and shunt active power filters.				
<b>3</b>	To make aware of research avenues in the field of power electronics.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Interpret configuration and working of various Power Electronic converters.			III	Applying
<b>CO2</b>	Analyze various Power Electronic converters and systems.			IV	Analyzing
<b>CO3</b>	Evaluate various power electronic systems using power electronic converters.			V	Evaluating
<b>List of Experiments / Lab Activities/Topics</b>					
Lab activities/Lab performance shall include mini-project, presentations, drawings, case studies, report writing, site visit, lab experiment, tutorials, assignments, group discussion, programming and other suitable activities, as per the nature and requirement of the lab course.					
<b>Textbooks</b>					
1	M. H. Rashid, "Power Electronics: circuits devices and applications", Pearson Education, Third edition.				
<b>References</b>					
1	B. K. Bose, "Modern Power Electronics and AC drives", PHIPL, New Delhi.				
2	M. B. Patil, V. Ramayanan and V. T. Ranganathan, "Simulation of Power Electronics circuits", Narosa publication.				
	Remus Teodorescu, Marco Liserre and Pedro Rodrigues, "Grid- Converters for Photovoltaic and Wind Power Converters", A John Wiley and sons Ltd., first edition 2011.				
	IEEE Transaction papers.				
<b>Useful Links</b>					
1	NPTEL lectures on Advanced Power Electronics				

**CO-PO Mapping****Programme Outcomes (PO)**

	1	2	3	4	5	6
CO1			1			
CO2				1		
CO3				2		1

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

**Assessment**

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

<b>Walchand College of Engineering, Sangli</b> (Government Aided Autonomous Institute)					
<b>AY 2022-23</b>					
<b>Course Information</b>					
<b>Programme</b>	M. Tech. (Power System Engineering)				
<b>Class, Semester</b>	First Year M.Tech., Sem II				
<b>Course Code</b>	6PS572				
<b>Course Name</b>	Professional Elective 3: EHVAC Laboratory				
<b>Desired Requisites:</b>	Power System				
<b>Teaching Scheme</b>		<b>Examination Scheme (Marks)</b>			
<b>Practical</b>	2 Hrs/ Week	<b>LA1</b>	<b>LA2</b>	<b>Lab ESE</b>	<b>Total</b>
<b>Interaction</b>	--	30	30	40	100
<b>Credits: 1</b>					
<b>Course Objectives</b>					
<b>1</b>	To understand the breakdown mechanisms in gaseous, liquid and solid insulation.				
<b>2</b>	To understand methods of generation and measurement of high voltage, impulse voltage and impulse current.				
<b>3</b>	To lay a foundation for higher studies in extra high voltage ac.				
<b>Course Outcomes (CO) with Bloom's Taxonomy Level</b>					
At the end of the course, the students will be able to,					
<b>CO</b>	<b>Course Outcome Statement/s</b>			<b>Bloom's Taxonomy Level</b>	<b>Bloom's Taxonomy Description</b>
<b>CO1</b>	Summarize breakdown mechanisms in gaseous, liquid and solid insulations.			II	Understanding
<b>CO2</b>	Understand the basic generation and measurement of High voltage and High current for testing purposes			II	Understanding
<b>CO3</b>	Analyze the HV generation equipment and their application.			IV	Analyzing
<b>List of Experiments / Lab Activities/Topics</b>					
Lab activities/Lab performance shall include mini-project, presentations, drawings, case studies, report writing, site visit, lab experiment, tutorials, assignments, group discussion, programming and other suitable activities, as per the nature and requirement of the lab course.					
<b>Textbooks</b>					
1	Rakosh Das Begamudre, "EHVAC Transmission Engineering", Wiley Eastern Limited, 3rd Edition 2008.				
<b>References</b>					
1	Twian Gonen, "EHVAC and HVDC Transmission System Engineering – Analysis and Design" John Wiley and Sons 1988.				
2	EHVAC and HVDC Transmission Engineering & Practice: S.V. Rao				
3	Twian Gonen, "Electric Power Transmission System Engineering-Analysis and Design", John Wiley and Sons 1988.				
<b>Useful Links</b>					
1	Computer Usage / Lab Tool: MATLAB/TLS/Power world/MiPower Simulator				



**CO-PO Mapping****Programme Outcomes (PO)**

	1	2	3	4	5	6
CO1			2			
CO2				2		
CO3				2		

The strength of mapping is to be written as 1: Low, 2: Medium, 3: High  
Each CO of the course must map to at least one PO.

**Assessment**

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

Week 1 indicates starting week of a semester. Lab activities/Lab performance shall include performing experiments, mini-project, presentations, drawings, programming, and other suitable activities, as per the nature and requirement of the lab course. The experimental lab shall have typically 8-10 experiments and related activities if any.

# **Open Elective**

## Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

**AY 2022-23**

### Course Information

<b>Programme</b>	M.Tech. (Power System Engineering)
<b>Class, Semester</b>	First Year M. Tech., Sem II
<b>Course Code</b>	6OE506
<b>Course Name</b>	Open Elective: Control Techniques for Electrical Drives
<b>Desired Requisites:</b>	M.Tech. (Power System Engineering)

Teaching Scheme		Examination Scheme (Marks)			
<b>Lecture</b>	3 Hrs/week	<b>MSE</b>	<b>ISE</b>	<b>ESE</b>	<b>Total</b>
<b>Tutorial</b>	--	30	20	50	100
<b>Credits: 3</b>					

### Course Objectives

<b>1</b>	To provide the latest knowledge in the field of electrical drives.
<b>2</b>	To provide sufficient knowledge in the area of advanced control techniques for induction motor and synchronous machines.
<b>3</b>	To make the student aware of the research in the field of electrical drives.

### Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

CO	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
<b>CO1</b>	Explain various concept used in AC and DC drives.	II	Understanding
<b>CO2</b>	Apply control techniques to AC and DC drives.	III	Applying
<b>CO3</b>	Analyze control techniques for AC and DC drives.	IV	Analyzing
<b>CO4</b>	Evaluate various control schemes of AC and DC drives.	V	Evaluating

Module	Module Contents	Hours
I	<b>Basics of drives</b> Types & parts of the Electrical drives, fundamental torque equation, speed torques characteristics DC motor & Induction motor, multi quadrant operation of the drive, classification of mechanical load torques, steady state stability of the drive, constant torque and constant HP operation of the drive, closed loop speed control.	4
II	<b>DC motor drives</b> Methods of speed control, starting and breaking operation, single phase and three phase full controlled and half controlled converter fed DC drives, Multi quadrant operation of separately excited DC shunt motor, dual converter fed DC drives, circulating and non – circulating mode of operation, chopper control of DC shunt motor drives, four quadrant operation of chopper fed DC shunt motor drive.	5
III	<b>Induction motor drives</b> Speed control methods for three phase induction motor, VSI fed induction motor drive, constant torque (constant E/F and constant V/F), constant HP operation, closed loop speed control block diagram., CSI fed induction motor drive, speed torque characteristics of CSI fed drive, closed loop speed control block diagram, comparison of CSI fed and VSI fed induction motor drive, Stator voltage control. Chopper controlled resistance in rotor circuit, slip power recovery using converter cascade in rotor circuit, sub synchronous and super synchronous speed control, Kramer speed control.	5

IV	<b>Modeling of Induction Motor and PWM Techniques</b> abc – dq transformation, transformation from stationary reference frame to synchronously rotating reference frame and vice versa. Equivalent circuits of induction motor in dynamic dq stationary and synchronously rotating reference frame. Permanent magnet synchronous machine dq equivalent circuits. The three phase six step bridge inverter, three phase PWM inverter, PWM techniques such as sinusoidal PWM, hysteresis band current control PWM.	5
V	<b>Vector Control and Direct Torque Control of Induction Motor</b> Vector control of induction motor, DC drive analogy, equivalent circuit, phasor diagram. Direct rotor flux oriented vector control and indirect rotor flux oriented vector control, stator flux oriented vector control. Torque equation of IM in terms of stator and rotor flux, direct torque and flux control method (DTC) and self-commissioning of the drive.	5
VI	<b>Synchronous motor and SRM Drives</b> VSI fed synchronous motor drives, true synchronous and self-control mode, open loop and closed loop speed control of Permanent magnet synchronous machine, brushless DC motor drives. Switched reluctance motor drives, torque equation, converter circuits, operating modes and applications. Solar panel VI characteristics, solar powered pump, maximum power point tracking and battery operated vehicles.	4
<b>Textbooks</b>		
1	G. K. Dubey, “ <i>Fundamentals of Electrical Drives</i> ”, Narosa publication, 2nd edition, 2002.	
2	B. K. Bose, “ <i>Modern Power Electronics and AC drives</i> ”, Prentice Hall of India Pvt. India, 1986.	
<b>References</b>		
1	Peter Vas, “ <i>Vector Control of AC machines</i> ”, Clarendon Press Oxford, 1999.	
2	Ned Mohan, “ <i>Advanced Electrical drives – Analysis, control and modeling using Simulink</i> ”, John Wiley and sons, 2001.	
3	P. S. Bhimra, “ <i>Power Electronics</i> ”, 2nd edition, Khanna Publishers.	
<b>Useful Links</b>		
1	NPTEL video lectures on Electrical Drives	

<b>CO-PO Mapping</b>						
<b>Programme Outcomes (PO)</b>						
	1	2	3	4	5	6
CO1				1		
CO2				1		
CO3			1	2		
CO4			1	3		
The strength of mapping is to be written as 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO.						

<b>Assessment</b>
<p>The assessment is based on MSE, ISE and ESE.</p> <p>MSE shall be typically on modules 1 to 3.</p> <p>ISE shall be taken throughout the semester in the form of teacher’s assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.</p> <p>ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.</p> <p>For passing a theory course, Min. 40% marks in (MSE+ISE+ESE) are needed and Min. 40% marks in ESE are needed. (ESE shall be a separate head of passing)</p>